

100

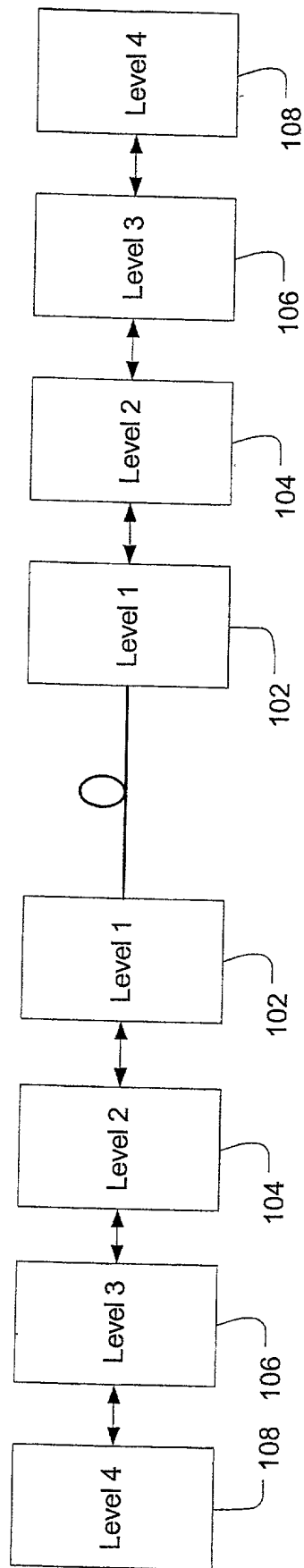


FIG. 1

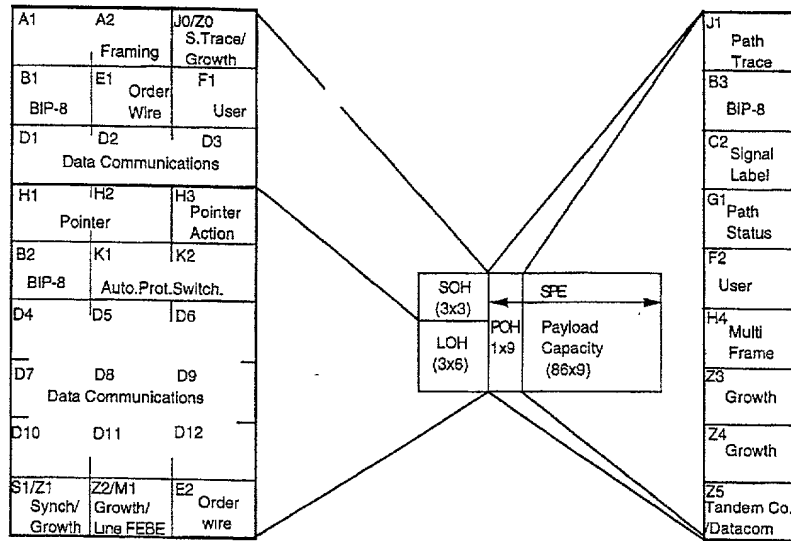


FIG. 2  
Conventional Art

Byte	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Label	FS	T	I	X	X	X	X	X	X	X	X	P1	P2	P3	P4	P5
Description	CRC	Level	DisId	Node Identifier								Port Identifier				
Field	F0	F1	F2	F3								F4				

FIG. 3

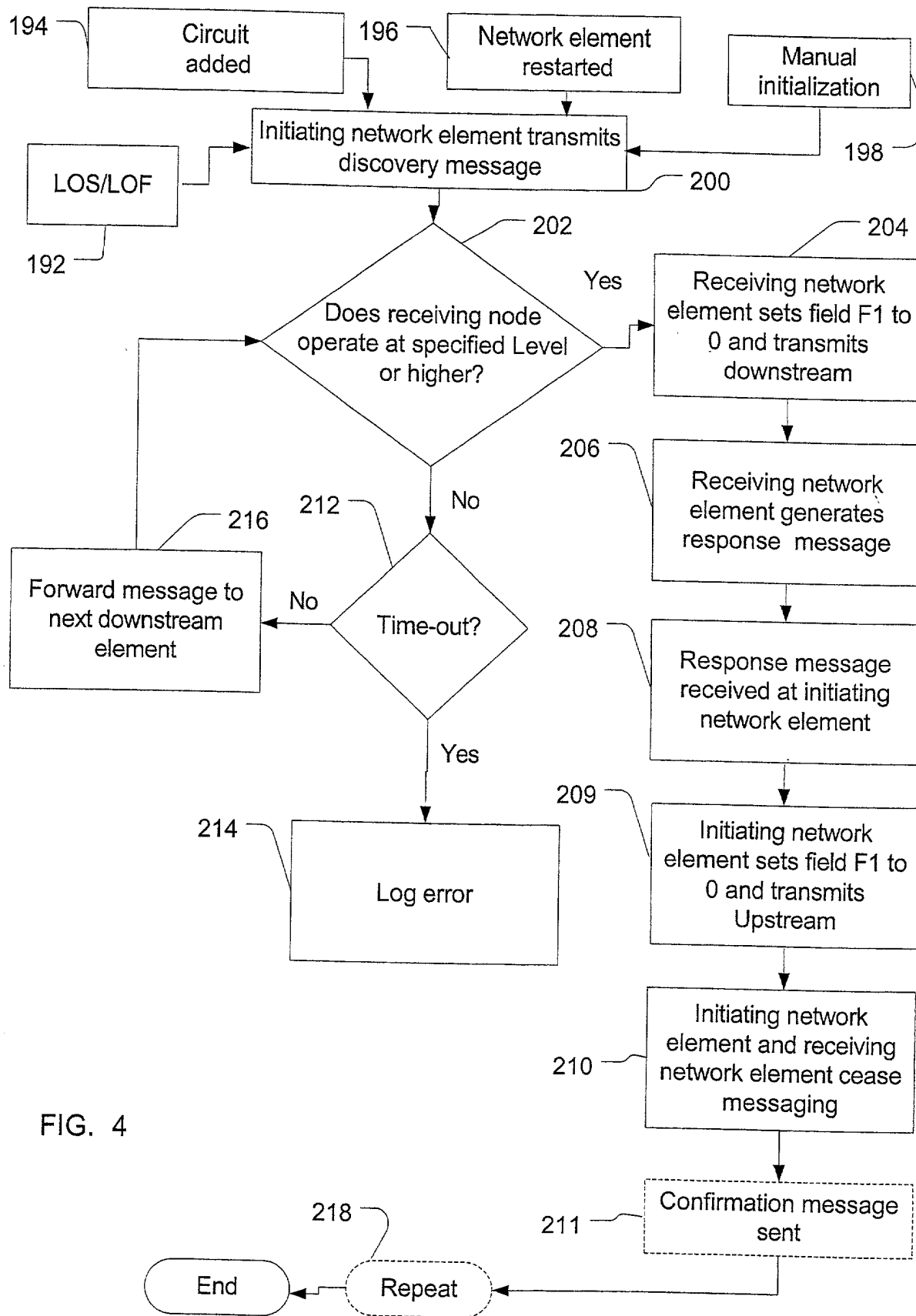


FIG. 4

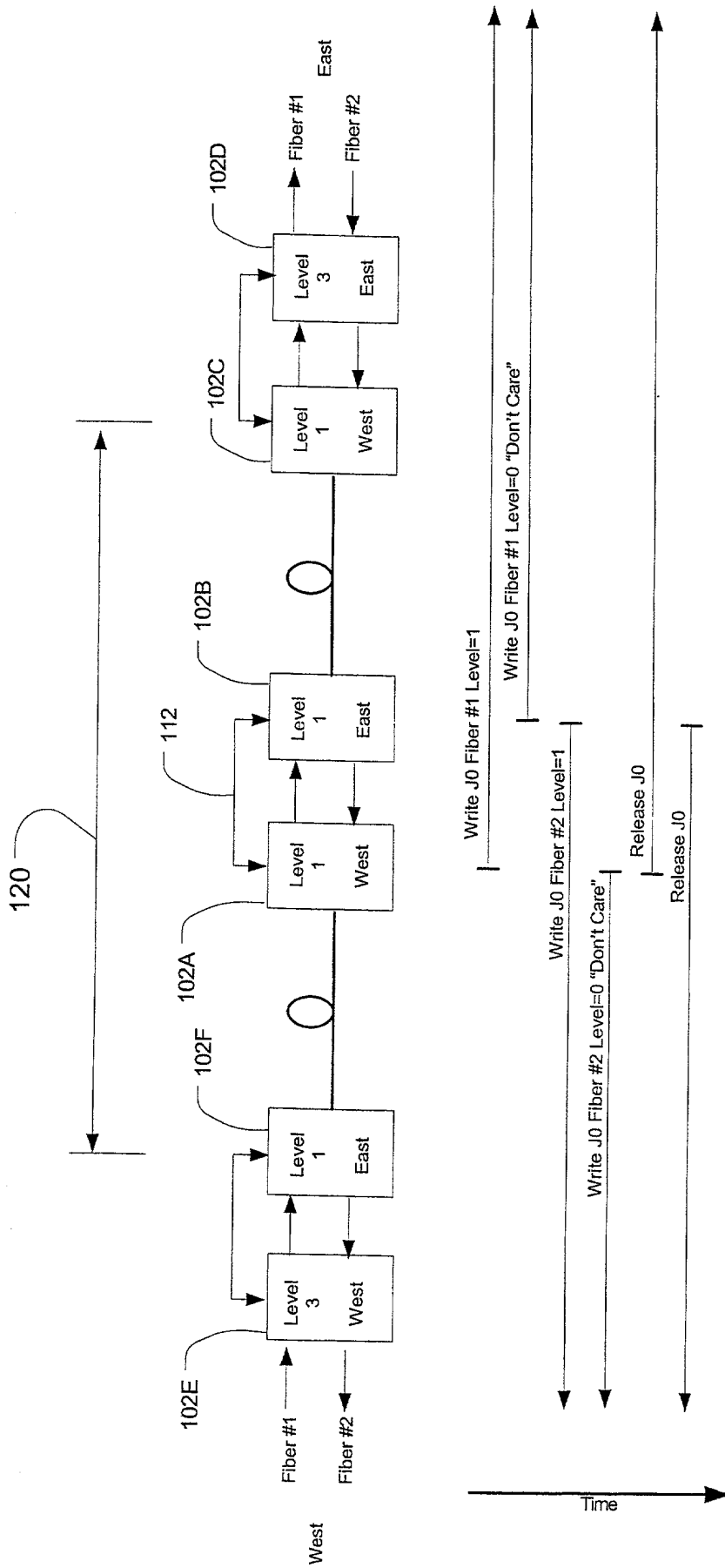


FIG. 5A



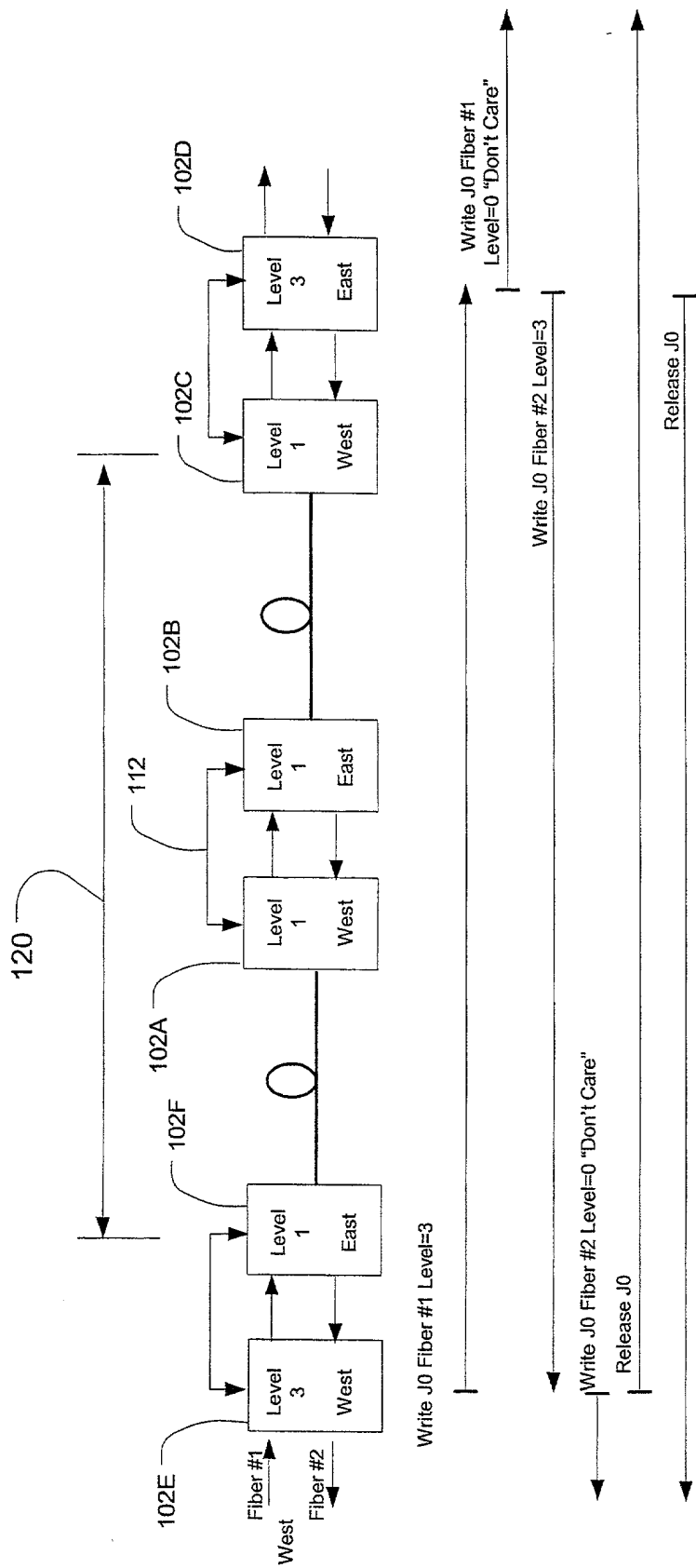
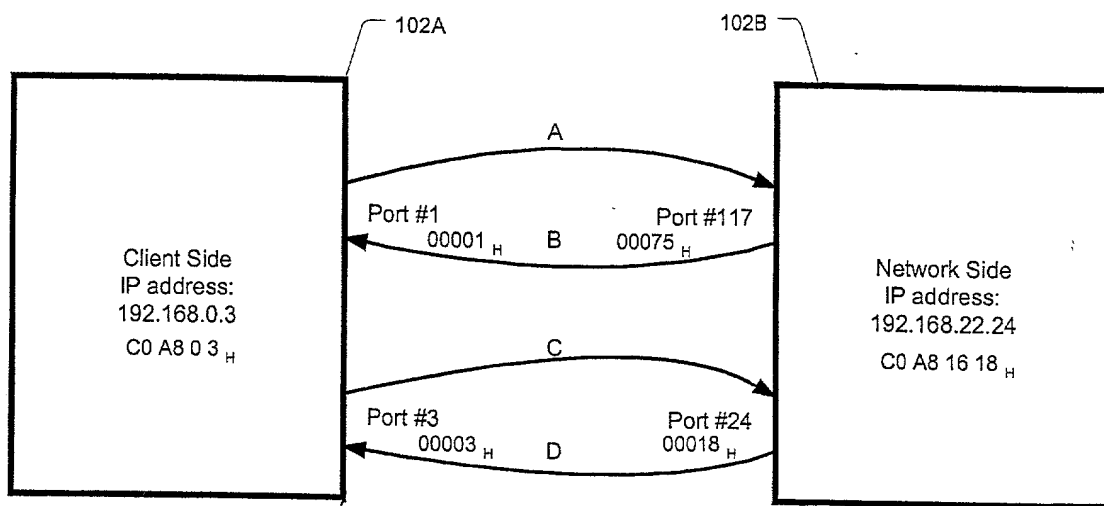


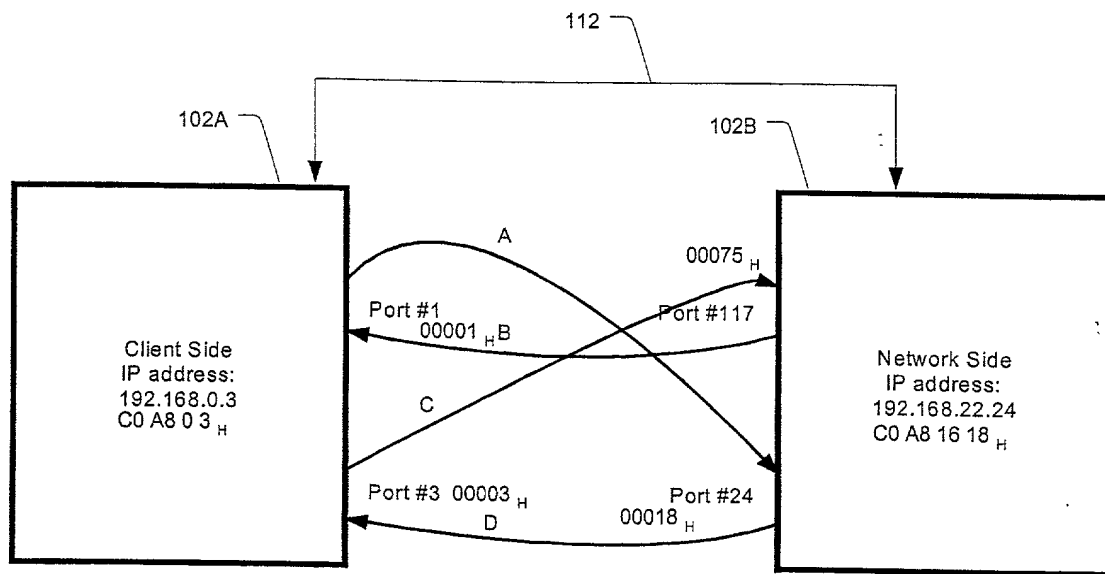
FIG. 5C



- A 1 P C0A80003 00001
- B 1 P C0A81618 00075
- C 1 P C0A80003 00003
- D 1 P C0A81618 00018

FIG. 6





A 1 P C0A80003 00001

B 1 P C0A81618 00075

C 1 P C0A80003 00003

D 1 P C0A81618 00075

FIG. 7

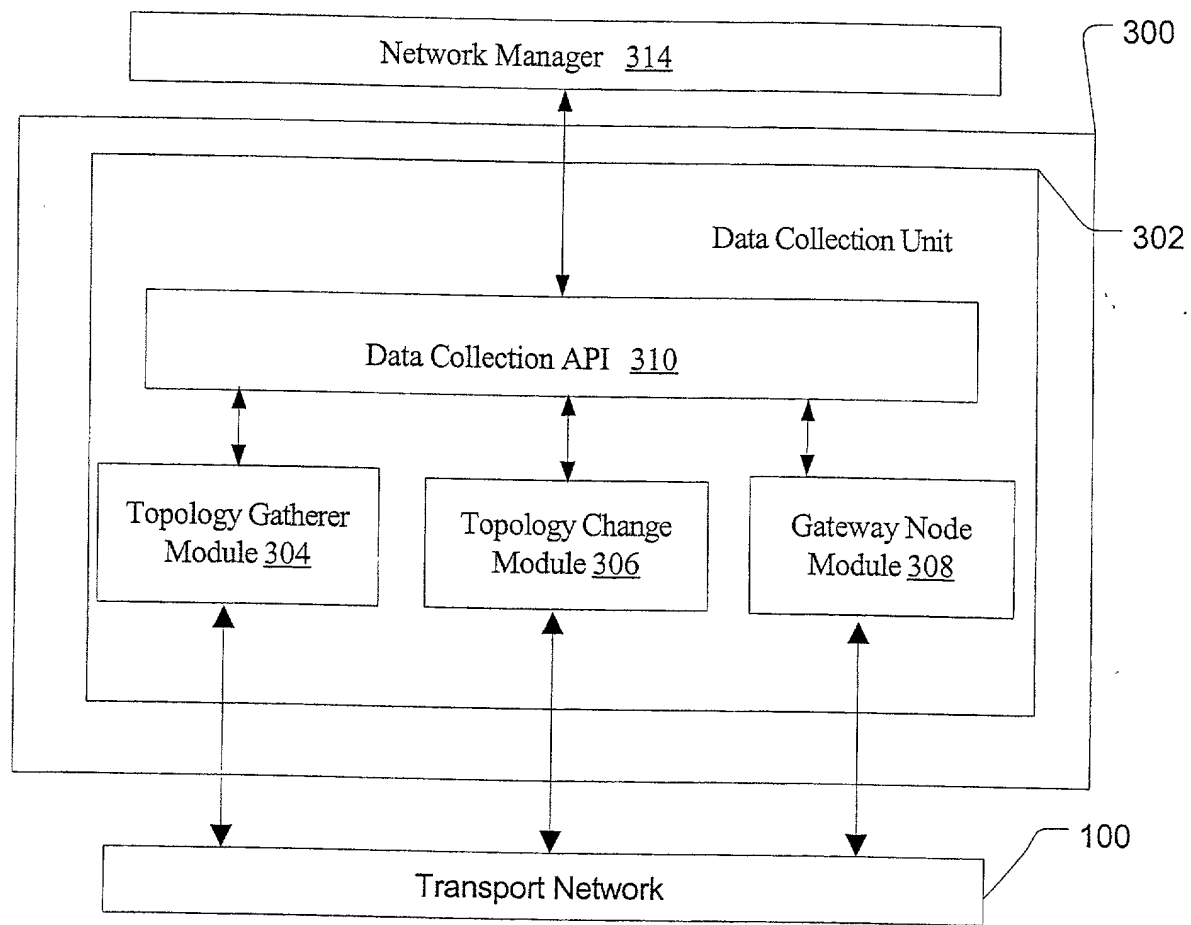


FIG. 8

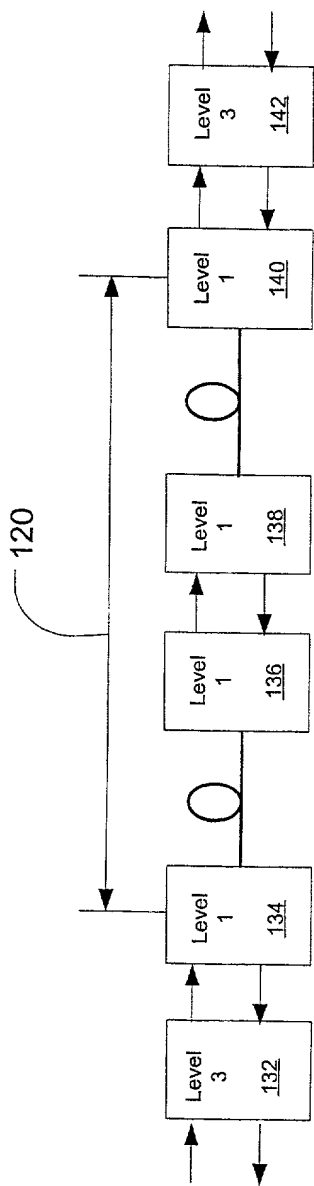


FIG. 9

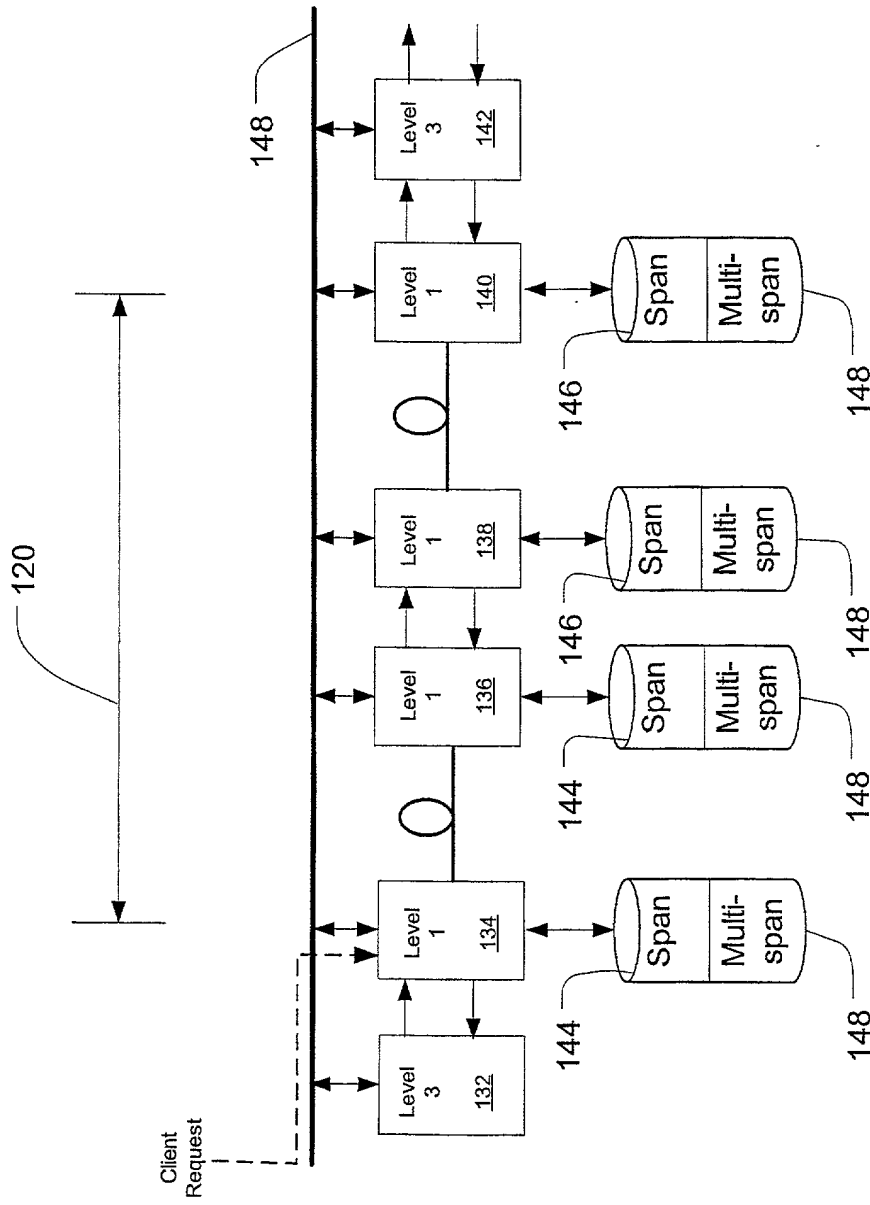
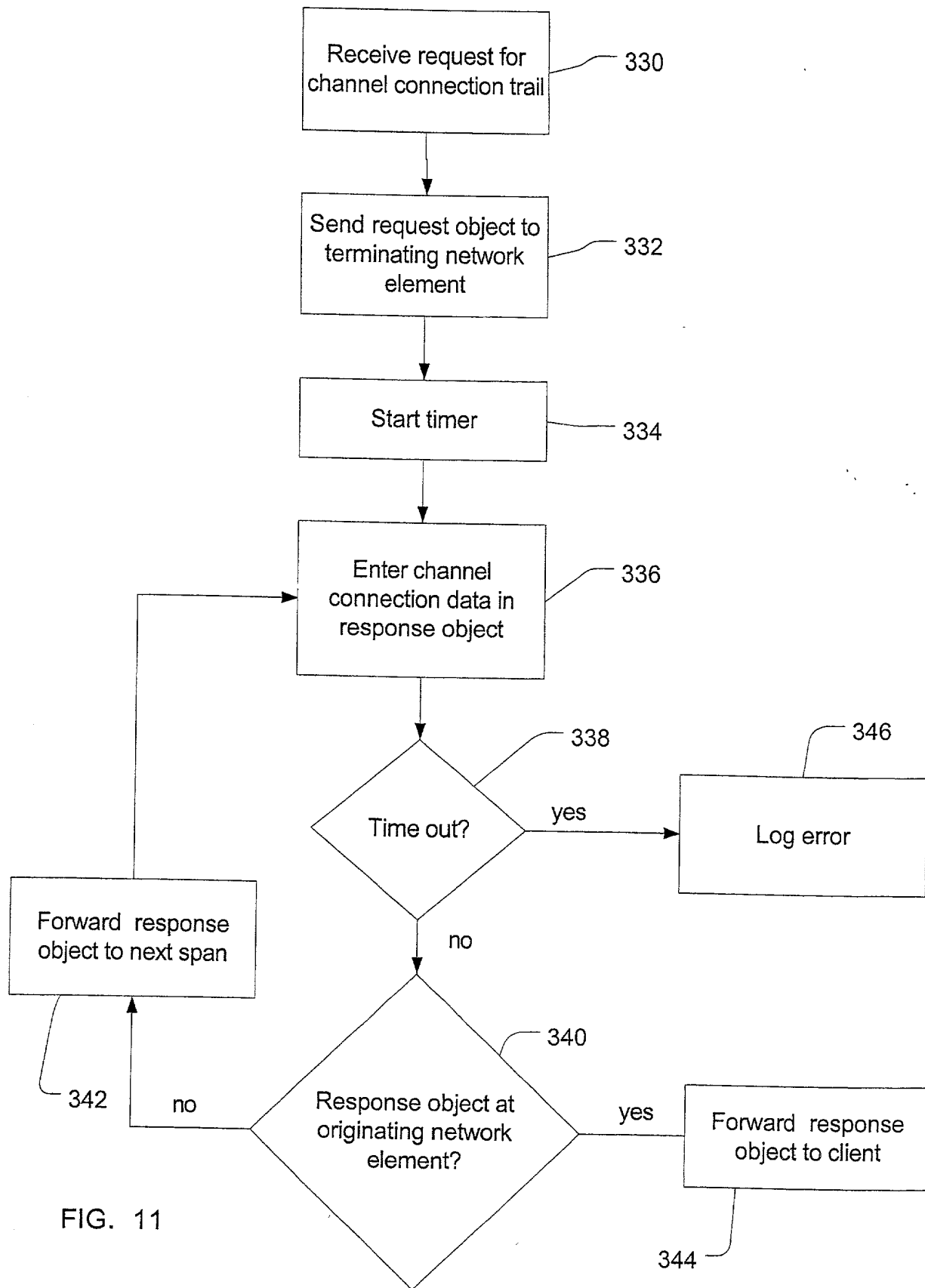


FIG. 10



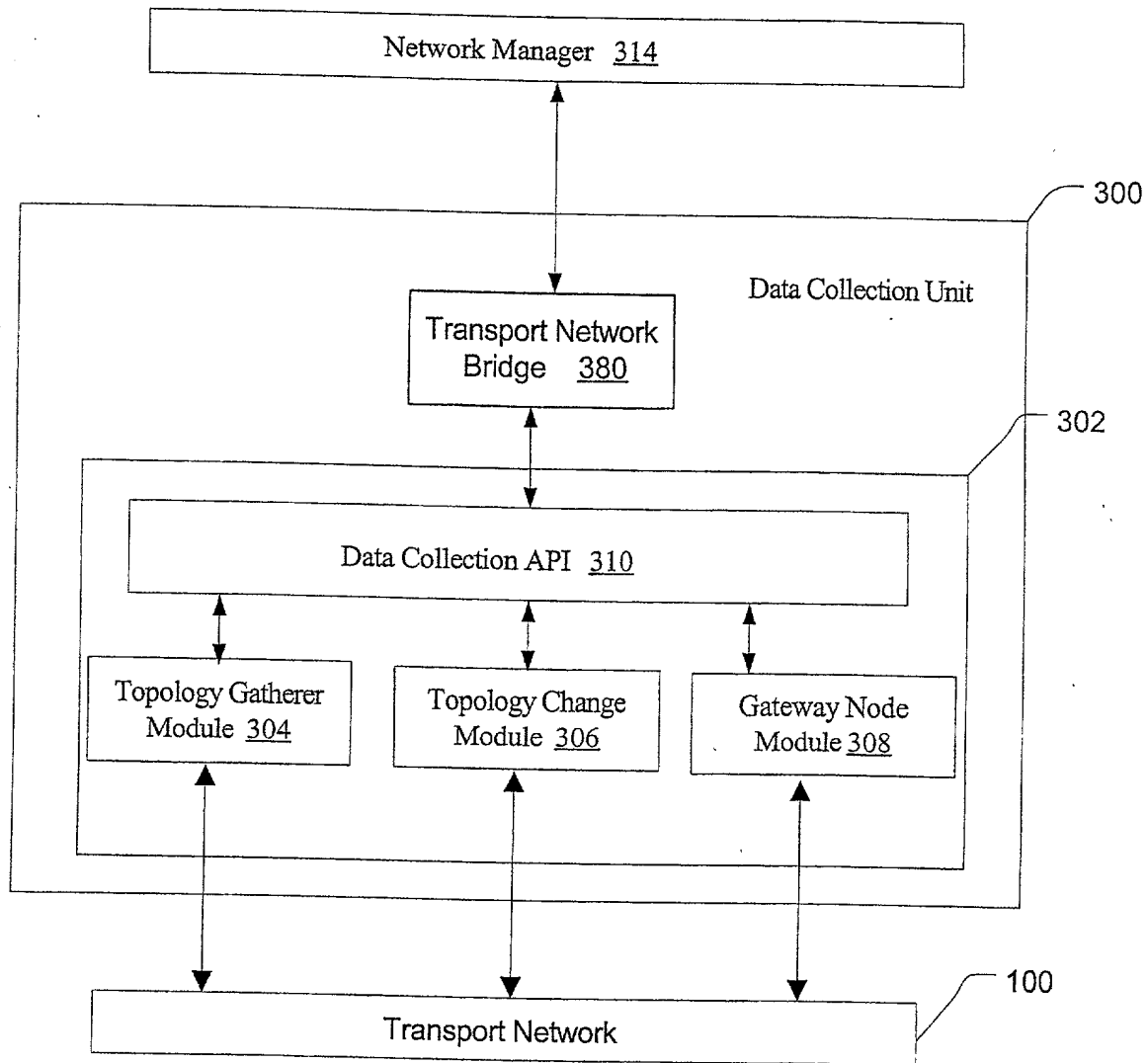


FIG. 12

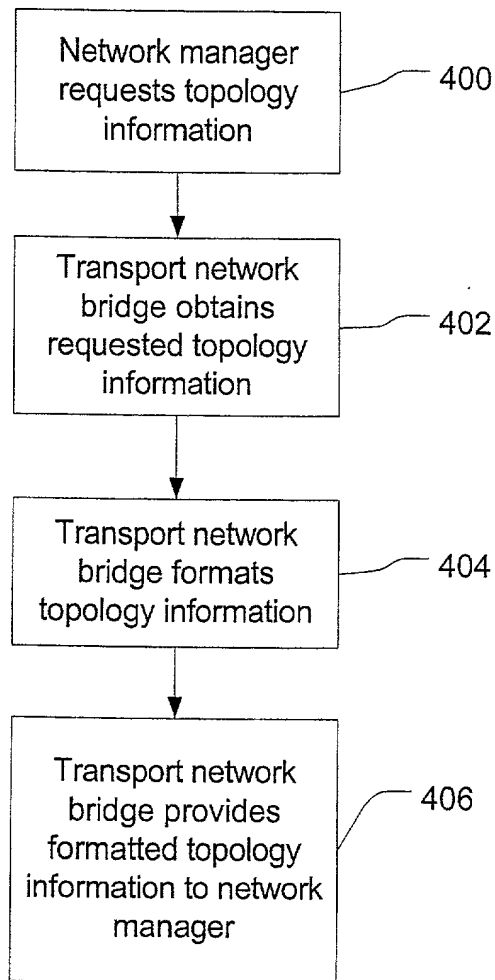


FIG. 13

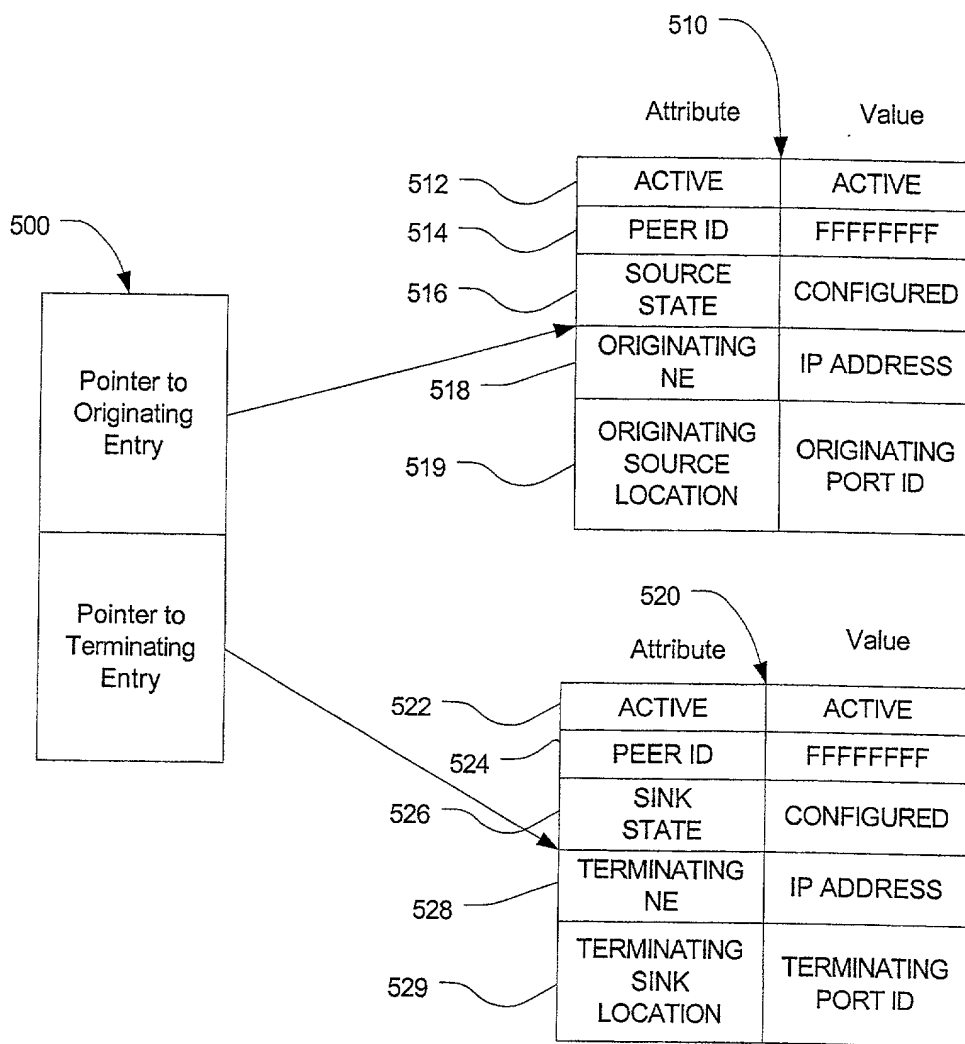


FIG. 14



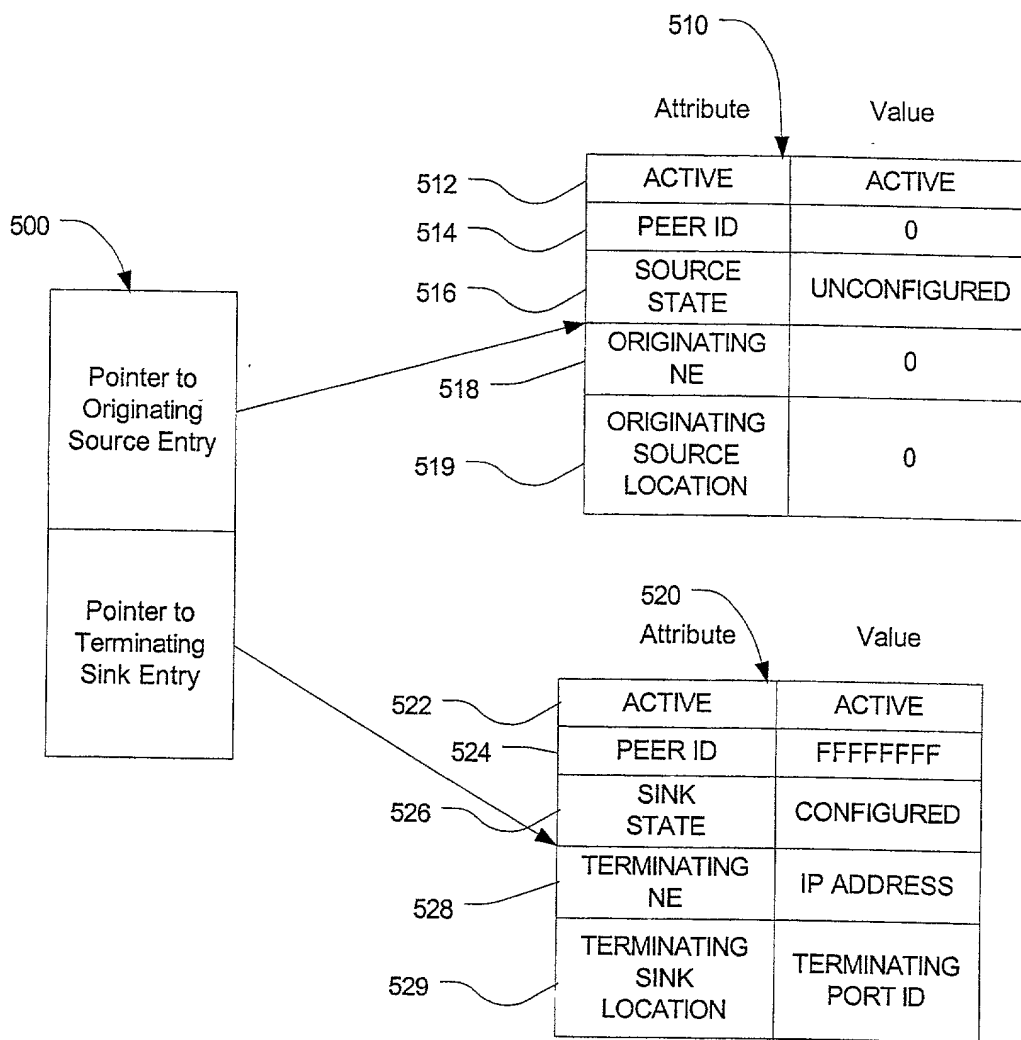


FIG. 15

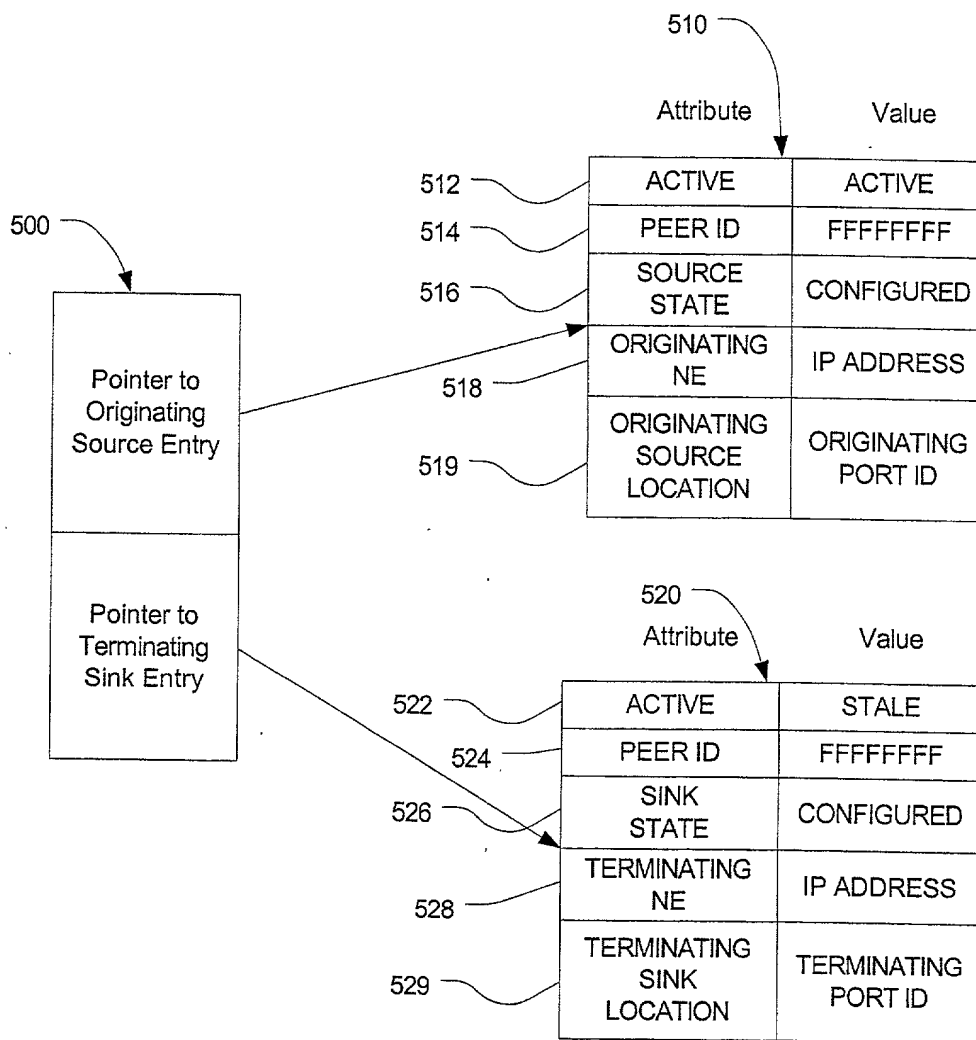


FIG. 16

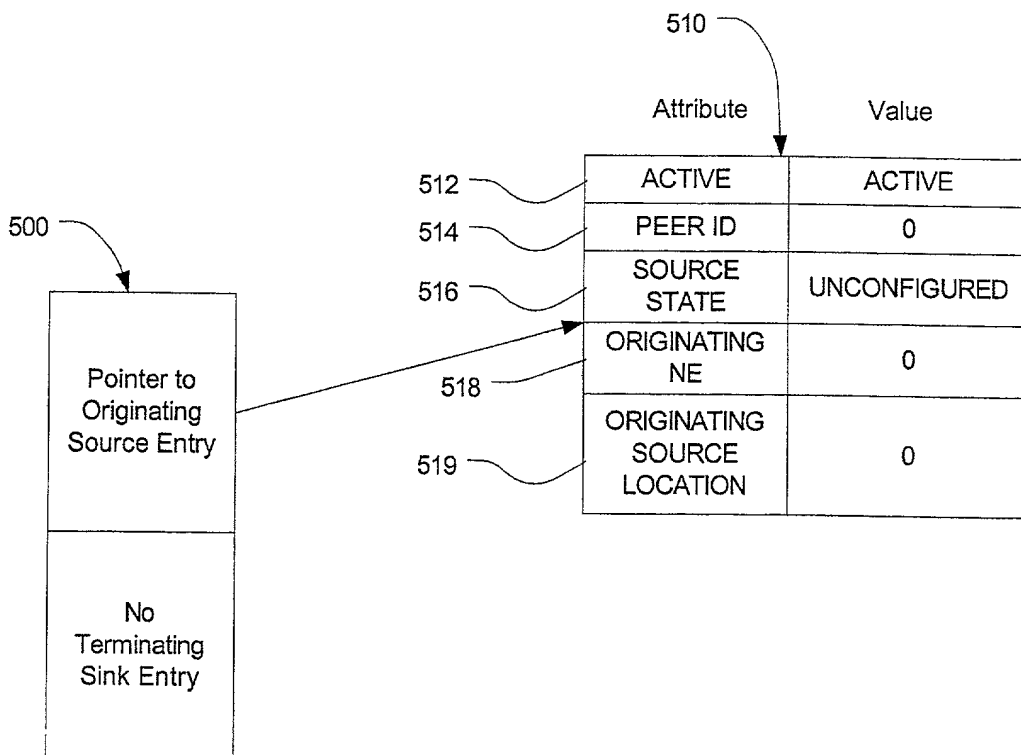


FIG. 17